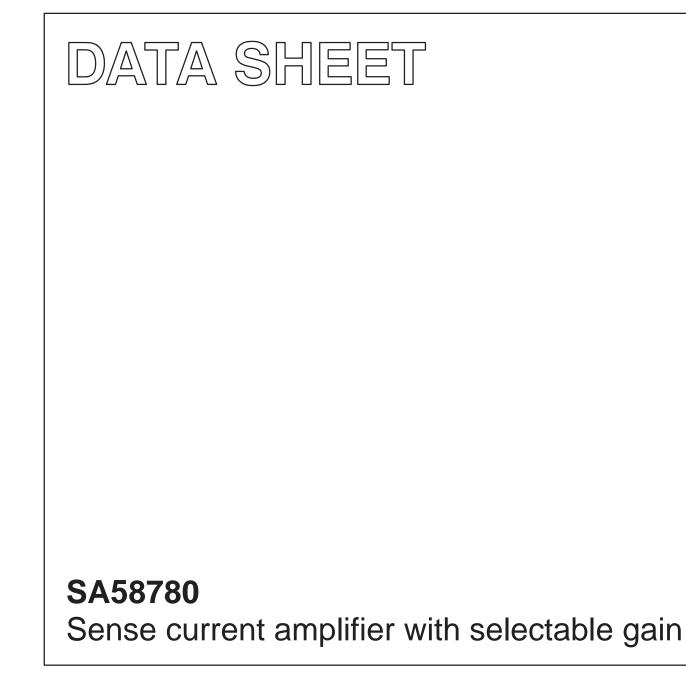
INTEGRATED CIRCUITS



Product data

2001 Oct 03

File under Integrated Circuits, Standard Analog



GENERAL DESCRIPTION

The SA58780 is a single amplifier that allows current sensing independent of the supply voltage. The input offset voltage is typically $\pm 500 \ \mu$ V with typical offset drift of $\pm 6 \ \mu$ V/°C. The SA58780 supply current is typically 150 μ A and it operates from 3.0 V to 24 V single supply. The input common mode range is selectable for high and low ranges. The amplifler gain is user selected for a "High" of 100 V/V or a "Low" of 50 V/V.

The SA58780 is ideal for battery charger applications in notebook computers and PDAs.

FEATURES

- Supply voltage range: 3 V to 24 V
- Low supply current: 150 μA (typical)
- Low input offset voltage: ±500 μV (typical)
- Low input offset drift: ±6 μV/°C (typical)
- Power supply rejection ratio (1 kHz): 80 dB (typical)
- Common mode rejection ratio (1 kHz): 100 dB (typical)
- Common mode input range selection:
 1.8 V to 24 V (I_{SEL} HIGH);
 -0.3 V to V_{CC} 2.4 V (I_{SEL} LOW)
- Amplifier gain selection:

TO CHARGE

 G_{SEL} HIGH: $G_V = 100$ V/V; G_{SEL} LOW: $G_V = 50$ V/V

SIMPLIFIED DEVICE DIAGRAM

Rs

TO BATTERY

I_{SEI}

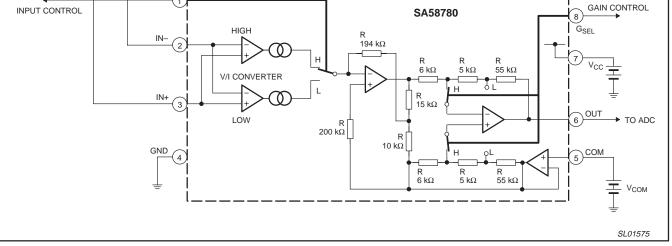


Figure 1. Simplified device diagram.

2

APPLICATIONS

- Notebook computers
- Personal digital assistants (PDA)

SA58780

ORDERING INFORMATION

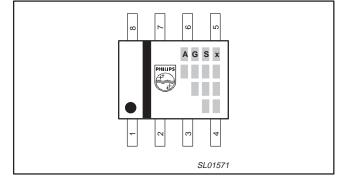
Philips Semiconductors

TYPE NUMBER	PACKAGE	PACKAGE		
ITPE NUMBER	NAME	DESCRIPTION	RANGE	
SA58780D	SO8	plastic small outline package; 8 leads; body width 3.9 mm	–20 to +85 °C	

Part number marking

Each device is marked with three or four lines of alphanumeric codes. The first three letters of the top line designate the product. The fourth letter, represented by 'x', is a date tracking code. The remaining lines are for manufacturing codes.

Part number	Marking
SA58780D	AGSX



PIN CONFIGURATION

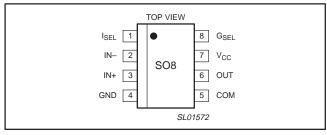


Figure 2. Pin configuration.

PIN DESCRIPTION AND EQUIVALENT CIRCUITS

PIN	SYMBOL	DESCRIPTION	INTERNAL EQUIVALENT CIRCUIT
1	I _{SEL}	Input common mode range selection HIGH: 1.8 V to 24 V LOW: –0.3 V to V _{CC} – 2.4 V	V _{cc} (7)
4	GND	Ground	
2	IN–	Inverting input	
3	IN+	Non-inverting input	

SA58780

Philips Semiconductors

Product data

Sense current amplifier with selectable gain

SA58780

PIN	SYMBOL	DESCRIPTION	INTERNAL EQUIVALENT CIRCUIT
5	СОМ	Reference voltage input	V _{CC} 7
6	OUT	Output	V _{cc} 7
7	V _{CC}	Positive supply	GND 4
8	G _{SEL}	Gain selection HIGH: 100 V/V LOW: 50 V/V	V _{CC} (7) G _{SEL} (8) GND (4)

MAXIMUM RATINGS

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V _{CC}	Single supply voltage	-0.3	+25	V
V _{IN}	Input voltage	-0.3	+25	V
T _{stg}	Storage temperature	-40	+125	°C
T _{amb}	Operating ambient temperature	-20	+85	°C
P _D	Power dissipation	-	300	mW

SA58780

ELECTRICAL CHARACTERISTICS

 $V_{CC} = 5.0 \text{ V}; \text{ } V_{ICM} = 15 \text{ } V; \text{ } V_{COM} = 25 \text{ } V; \text{ } V_{ISEL} = 5 \text{ } V; \text{ } V_{GSEL} = 5 \text{ } V; \text{ } R_L = 10 \text{ } \text{k}\Omega; \text{ } T_{amb} = 25 \text{ }^{\circ}\text{C}, \text{ } \text{unless otherwise specified}.$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CC}	Supply voltage operating range		3.0	-	24	V
I _{CC}	Supply current		-	150	200	μA
G _{v(high)}	Voltage gain HIGH	G _{SEL} = 5 V	97	100	103	mV/mV
G _{v(low)}	Voltage gain LOW	G _{SEL} = 0 V	48.5	50	51.5	mV/mV
V _{IO1}	Input offset voltage 1	$\Delta V_{IN} = 0 V; V_{ISEL} = 5 V (HIGH)$	-0.5	-	0.5	mV
V _{IO2}	Input offset voltage 2	$\Delta V_{IN} = 0 V; V_{ISEL} = 0 V (LOW)$	-0.5	-	0.5	mV
ΔV_{IO1}	Input offset voltage 1 temperature coefficient	V _{ISEL} = 5 V	-4	-	4	μV/°C
ΔV_{IO2}	Input offset voltage 1 temperature coefficient	V _{ISEL} = 0 V	-6	-	6	μV/°C
V _{I(CM)1}	Common mode input voltage range 1	V _{ISEL} = 5 V (HIGH)	1.8	-	24	V
V _{I(CM)2}	Common mode input voltage range 1	V _{ISEL} = 0 V (LOW)	-0.3	-	V _{CC} – 2.4	V
V _{I(dif)}	Differential input voltage		-200	-	200	mV
I _{i(bias)1}	Input bias current 1	$\Delta V_{IN} = 0 V; V_{ISEL} = 5 V (HIGH)$	0.8	1.2	1.6	μΑ
I _{i(bias)2}	Input bias current 2	$\Delta V_{IN} = 0 V; V_{ISEL} = 0 V (LOW)$	-0.8	-1.2	-1.6	μA
$\Delta V_{IO} / \Delta T$	Input offset voltage temperature drift	$T_{amb} = -20$ to +75 °C	-	±1	±3	μV/°C
Zi	Input impedance		100	-	-	kΩ
V _{COM}	COM voltage range	R _L = open	1.2	-	V _{CC} – 1.2	V
I _{ISEL}	I _{SEL} current	V _{ISEL} = 5 V	-	1.0	-	μA
V _{ISEL1}	I _{SEL} voltage range 1 (HIGH)		1.7	-	24	V
V _{ISEL2}	I _{SEL} voltage range 2 (LOW)		0	-	0.5	V
I _{GSEL}	G _{SEL} sink current	V _{GSEL} = 5 V	-	1.0	-	μA
V _{GSEL1}	G _{SEL} voltage range 1	(100 V/V)	1.7	-	24	V
V _{GSEL2}	G _{SEL} voltage range 2	(50 V/V)	0	-	0.5	V
V _{OUT}	Output voltage range	R _L = open	0.3	-	V _{CC} – 0.3	V
I _{O(source)}	Output source current	$V_{OUT} = V_{CC} - 0.3 V$	0.5	1.0	-	mA
I _{O(sink)}	Output sink current	V _{OUT} = 0.3 V	-0.5	-1.0	-	mA
f _{C1}	Cutoff frequency 1	$V_{GSEL} = 5 V (G_{v(high)} = 100 V/V);$ $V_{OUT} = -3 dB$	-	100	-	kHz
f _{C2}	Cutoff frequency 2		-	140	-	kHz
PSRR1	Power supply rejection ratio 1	f = 1 kHz; V _{ISEL} = 5 V	70	80	-	dB
PSRR2	Power supply rejection ratio 2	f = 1 kHz; V _{ISEL} = 0 V	70	80	-	dB
CMRR1	Common mode rejection ratio 1	f = 1 kHz; V _{ISEL} = 5 V	70	80	-	dB
CMRR2	Common mode rejection ratio 2	f = 1 kHz; V _{ISEL} = 0 V	70	80	-	dB

SA58780

APPLICATION INFORMATION

Battery current sensing circuit

The circuit shown in Figure 3 will sense when the load is drawing current from the battery, and the output of Pin 6 to an analog-to-digital converter can be used to provide a digital readout.

Pin 8, the Gain Select, is tied to ground. This gives a fixed G_v of 50 V/V. For a fixed G_v of 100 V/V, tie Pin 8 to V_{CC} . For selectable gain, Pin 8 may be connected to a user-controlled selector switch or the output of another device that will change state as the current rises and falls.

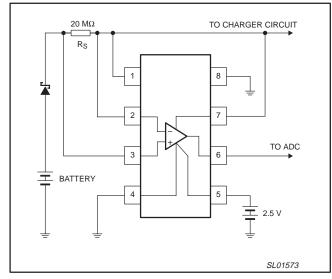


Figure 3. Battery current sensing circuit.

PACKING METHOD

The SA58780 is packed in reels, as shown in Figure 5.

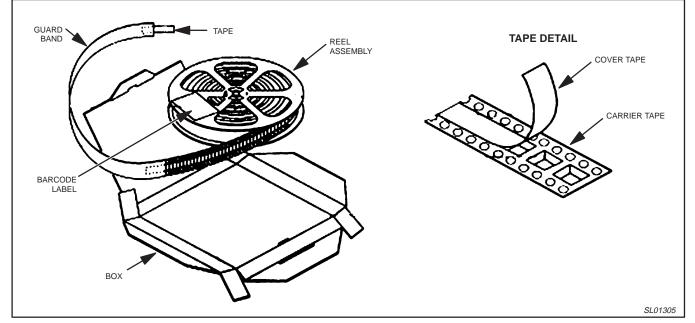


Figure 5. Tape and reel packing method

Charger current sensing

The only difference between the battery and charge current sense circuits is the diode position.

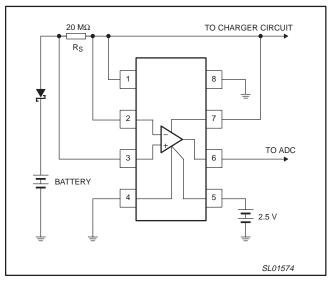
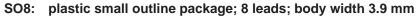


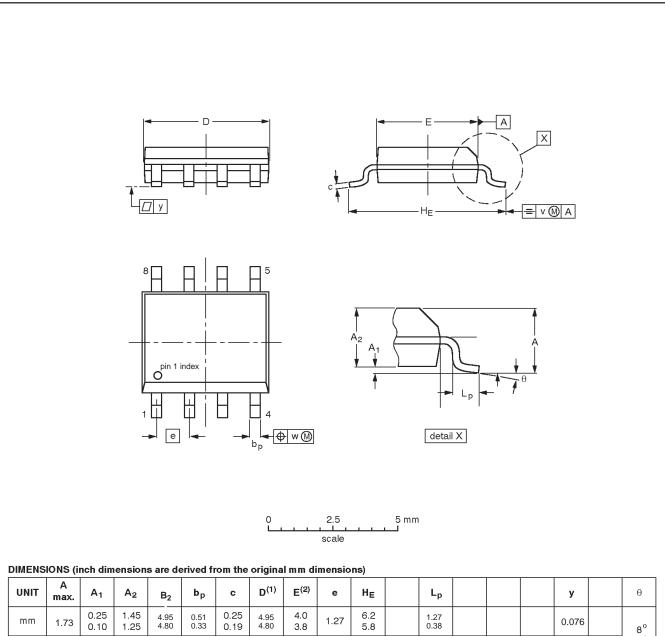
Figure 4. Charger current sensing circuit.

SA58780

õ°

0.003





Notes

inches

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

0.189

0.195

0.013

0.020

0.057

0.049

0.010

0.004

0.068

2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE	REFERENCES				
VERSION	IEC	JEDEC	EIAJ		
SO8	076E03	MS-012			

0.0100

0.0075

0.20

0.19

0.16

0.15

0.050

7

0.244

0.228

0.050

0.015

SA58780

Data sheet status

Data sheet status ^[1]	Product status ^[2]	Definitions
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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[1] Please consult the most recently issued data sheet before initiating or completing a design.

[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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